Name: Ariela Mitrani

Group Members: Jerry Kurtin, Garrett Guerrero, Jade Davis

Individual Design Contributions:

* Implemented the entire memory step, as well as most of write back IO and PC update IO
* Helped with wiring, timing diagrams, and control tables
* Completed corresponding sections of the lab report/write up

Advantages:

* I feel like this lab is what took me from barely understanding the content to completely understanding what goes on within a processor. Once I understood the theory, this project was pretty simple, and was just a lot of debugging
* Didn’t get stuck on certain parts that I would've not known how to do alone because I had a group I could lean on

Disadvantages:

* N/A, I had a really good group so my experience was really good

Group Design Shoutout:

* Garrett’s register file was awesome and perfectly implemented, making write back and decode much easier
* Jerry’s help debugging for matrix multiplication was very appreciated
* The only room for improvement I think we could implement is some sort of pipelining; Right now, we insert a few extra bubbles without pipelining, and it would make the program more efficient. However, it isn’t in the scope, so I’m happy with this project overall